

DINESH B. SONAWANE

M.Sc. [Electronic Science]

M.B.A. (Marketing)



Career Objective:

I am looking forward to obtain a position in a leading organization where I am able to apply the knowledge gained through my master degree. I firmly believe in the principle of implementing my duties with dedication and determination. I am looking for good career growth opportunities where my talent and knowledge could be best subjected and utilized for the benefit of the organization and myself.

Career Profile Highlights:

- ✚ Analysis, design, monitoring applications & implementation of formal software development.
- ✚ Collected data from records and conducted basic research – Participated in team research projects and conducted individual research.
- ✚ Planned and implemented projects Organized and managed multiple projects successfully.
- ✚ Excellent database management, Analysis and Processing Skills
- ✚ Able to work with all types of faculty and Effective time management Skills.
- ✚ Effective Communication Skills and MS Office Proficient
- ✚ Resourceful in the completion of projects, effective at multi-tasking
- ✚ Highly trustworthy, discreet, trustworthy and ethical.
- ✚ Classroom management and Skilled in training undergraduate and graduate students in safe laboratory research and Class room teaching.

Professional Experience:

Sr.No.	Company / Organization	Position	Duration
1.	Modern College, Pune.	Assistant Professor	Dec. 2019 – till now
2.	ISRO – UOP SPPU CELL, Pune	Research fellow	Aug 2016 – Feb 2019
3.	A. M. College Hadapsar, Pune	Assistant Professor	Jul 2018 – Apr 2019
4.	Cyronics Instrument Pvt. Ltd. Pune	Application Engi.	May2016 - Aug2016
5.	Jampot Photonics R & D Pune	Project Intern	Jan 2015 - May 2016

- ✚ I worked with **ISRO-UOP** Project on “Studies on Glare Reduction Techniques for Indoor Illumination Systems using **Python and MATLAB.**”

Guide: Dr.(Mr.) A. D. Shaligram. (Dean of Sci and Tech., H.O.D. of Electronics Science Dept. SPPU, Pune.

Educational Qualifications:

✚ Pursued M.Sc. in Electronic Science [VLSI] at Savitribai Phule Pune University, Pune.

Exam	Board/University	Percentage	Month and Year of Passing
M.Sc.	Savitribai Phule Pune University Pune. (Dept. of Electronic Science)	3.86/6	July, 2015
B.Sc.	N.Y.N.C. College, Chalisgaon. (Dept. of Electronic Science) N.M.U. Jalgaon.	73.11	Dec, 2012
H.S.C.	Rashtriya Jr. College, Chalisgaon. Maharashtra State Board.	58.00	February, 2009
S.S.C.	M.V. Ranjangaon. Maharashtra State Board.	72.92	March, 2007

Academic Projects:

Sr.No.	Year	Project Title	Duration
1.	M.Sc. Sem-IV	Theoretical Analysis, Design and micro fabrication of Bragg Cell	5 months
2.	M.Sc. Sem-III	1. Switching Mode Power supply 2. Digital Thermometer 3. Auto Balancing System	2 months 1 month 1 month
3.	M.Sc. Sem-II	Sensor Interfacing with Instrumentation Amplifier.	1 month
4.	M.Sc. Sem-I	Design Phase Lock loop	2 months
5.	B.Sc.	Advanced Laser based Intruder Alarm	5 months

Technical Skill:

Programming languages	Assembly C, C++, VHDL, VERILOG, Python
Operating Systems	Windows XP, Windows - 10, Linux
Software known	Opti Grating, PS-Spice, Origin, DIALux, OrCAD Layout 9.2,
Computer basics	MS-CIT, Visual Basics 6.0, Tally ERP 9.0, Core JAVA.
Software's worked on	Cadence Virtuoso 9.1, Xilinx, MATLAB, LabVIEW.

Extra-Curricular Activity and Achievements:

Sr.No.	Participation	Organisation / Institute	Year
1.	National Conference Paper Presentation	Kaveri College of Arts, Science and Commerce College, Pune	2017
2.	Workshop on Intellectual Property Rights	Kaveri College of Arts, Science and Commerce College, Pune	2018
3.	2nd International Symposium on Physics and Technology of Sensors (ISPTS-2)	Yashwantrao Chavan Academy of Development Administration (YASHADA), Pune	2015
4.	HAM workshop	College of Engineering, Pune	2015
5.	Design Tools for Photonics Sensors and Waveguide Components	Savitribai Phule Pune University, Pune	2015
6.	National Conference on Advances in Electronics and Interdisciplinary Applications	Fergusson College, Pune	2015
7.	Robotics Workshop	Fergusson College, Pune	2015

Certification:

Sr.No.	Participation	Organisation / Institute	Year
1.	Electronic Instrument Design	North Maharashtra University, Jalgaon	2012
2.	CORE JAVA	All India Council for Tech Edu, Mumbai	2012
3.	TALLY ERP 9.2	College of Computer Accounting, Pune	2012
4.	English Language Course	Symbiosis International University, Pune	2016
6.	Scout Camp	Scout Camp, Kanlada, Jalgaon	2007
7.	Student Co-coordinator	N.Y.N.C. College, Chalisgaon	2012

Research Paper:

✚ “Studies on glare for indoor illumination system using Simulation Software & Python”.

Guide: Dr.(Mr.) A. D. Shaligram (Dean and HOD of Electronics Science Dept. SPPU, Pune)

✚ “Review of Lighting Simulation Software’s and study of a Room Model using DIALux Lighting Simulation Software”

Guide: Dr.(Mrs.) J.A. Bangali (HOD of Electronic & Comp. Science Dept. Kaveri College, Pune)
Dr.(Mr.) A. D. Shaligram (Dean and HOD of Electronics Science Dept. SPPU, Pune)

✚ “Theoretical analysis, design and microfabrication of Bragg cell”

Guide: Dr.(Mr.) A. D. Shaligram (Dean and HOD of Electronics Science Dept. SPPU, Pune)
Dr.(Mr.) Rabinder Henry (Director at Pralhad P. Chhabria Research Center, CEO Jampot Photonics Research and Development, Pune)

Personal Strengths:

- + Positive attitude
- + Good Communication Skill
- + Good Grasping Power

Interests:

- + Playing Cricket
- + Listening to Music
- + Travelling

Declaration:

“I hereby declare that the information furnished above is true to the best of my knowledge.”

DATE: - / / 2021

PLACE: - Pune.

Dinesh B.Sonawane.